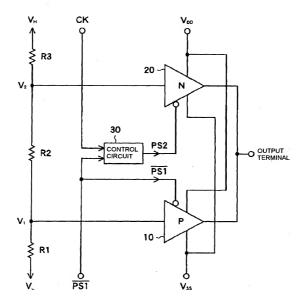
Title: SEMICONDUCTOR INTEGRATED CIRCUIT

Inventor: Takashi FUJISE Atty. Ref.: 9319S-000532

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FIG.1

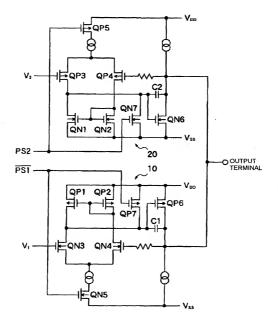


Title: SEMICONDUCTOR INTEGRATED CIRCUIT

Inventor: Takashi FUJISE Atty. Ref.: 9319S-000532

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FIG.2

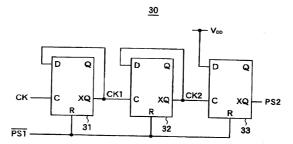


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FIG.3

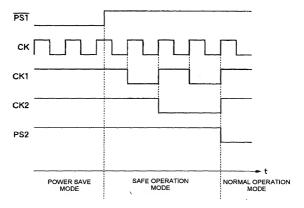


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FIG.4



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FIG.5

PRIOR ART

